Unit: mm

TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N CHANNEL IGBT

# GT30J322

### **FOURTH-GENERATION IGBT**

# CURRENT RESONANCE INVERTER SWITCHING APPLICATIONS

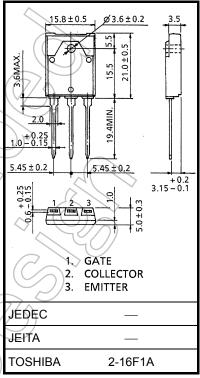
• FRD included between emitter and collector

• Enhancement mode type

• High speed :  $t_f = 0.25 \mu s$  (Typ.) (IC = 50A) • Low saturation voltage : VCE (sat) = 2.1V (Typ.) (IC = 50A)

### ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Emitter Voltage		V <sub>CES</sub>	600	$(\nearrow \land)$	
Gate-Emitter Voltage		V <sub>GES</sub>	±20	$\vee$	
Collector Current	DC	Ic	30	À	
	1ms	I <sub>CP</sub>	100		
Emitter-Collector Forward	DC	lF	30	Α	
Current	1ms	IFP	60	A	
Collector Power Dissipation (Tc = 25°C)		Pc	75	W	
Junction Temperature		T <sub>j</sub> (	150	°C	
Storage Temperature Range		T <sub>stg</sub>	55 to 150	°C	



Weight: 5.8 g (typ.)

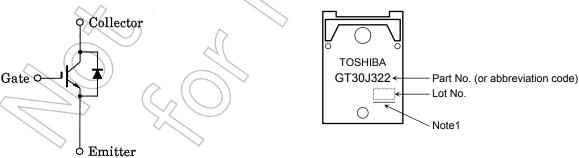
Note: Using continuously under heavy loads (e.g. the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).







Note1: A line under a Lot No. identifies the indication of product Labels. [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

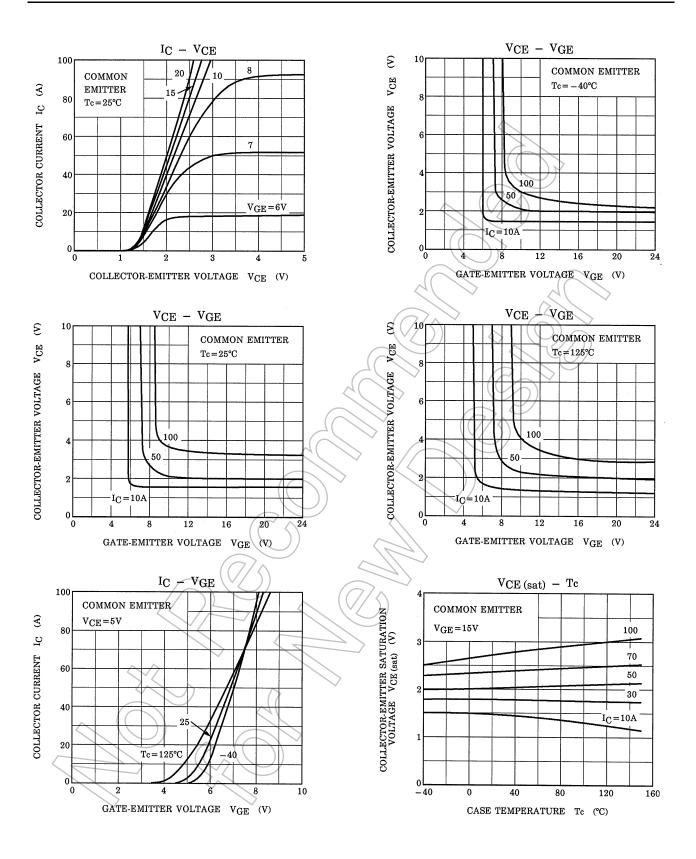
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

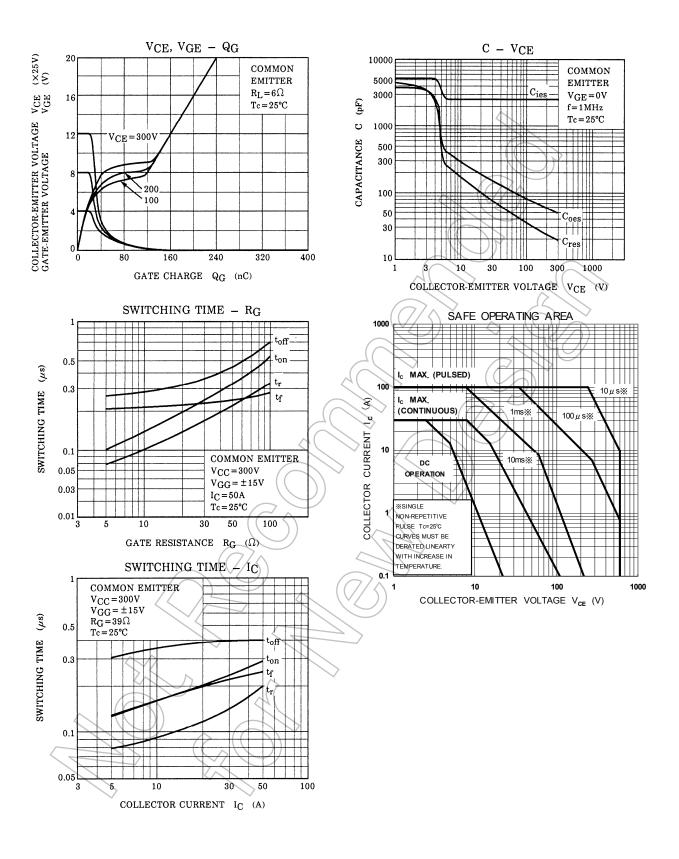
The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

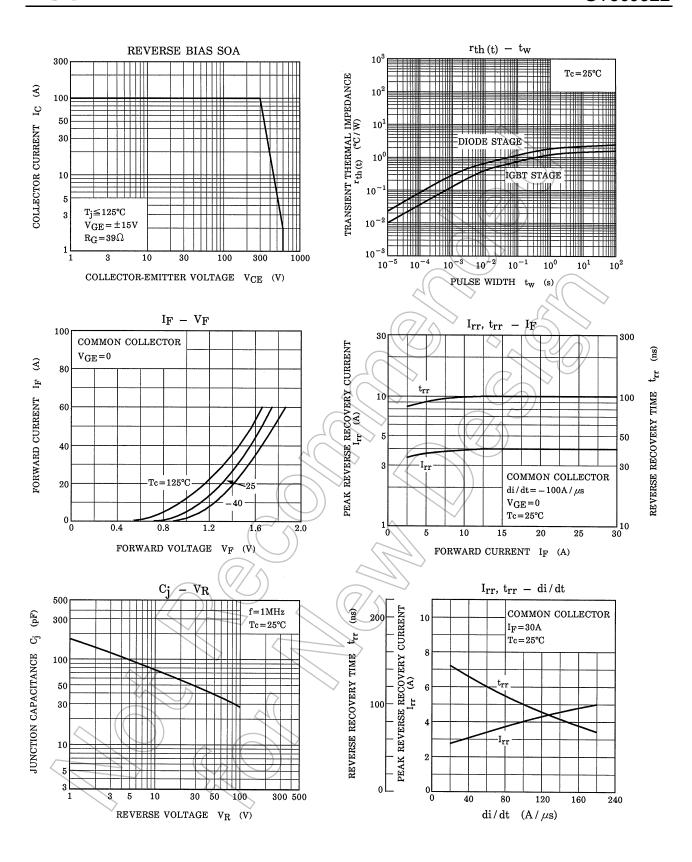
Start of commercial production 1998-11

## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Gate Leakage Curr	rent	I <sub>GES</sub>	V <sub>GE</sub> = ±20V, V <sub>CE</sub> = 0	_	_	±500	nA
Collector Cut-Off Current		I <sub>CES</sub>	V <sub>CE</sub> = 600V, V <sub>GE</sub> = 0	_	_	1.0	mA
Gate-Emitter Cut-	Off Voltage	V <sub>GE</sub> (OFF)	I <sub>C</sub> = 50mA, V <sub>CE</sub> = 5V	3.0	_	6.0	V
Collector-Emitter S	Saturation Voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 50A, V <sub>GE</sub> = 15V		2.1	2.8	V
Input Capacitance		C <sub>ies</sub>	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0, f = 1MHz	(F	2500	_	pF
Switching Time	Rise Time	t <sub>r</sub>	15V 0 1 2 39Ω 1 2 300V	7_	0.20	_	— µs
	Turn-On Time	t <sub>on</sub>		$\bigcirc )$	0.30	_	
	Fall Time	t <sub>f</sub>		_	0.25	0.40	
	Turn-Off Time	t <sub>off</sub>		_	0.40	_	
Peak Forward Voltage		V <sub>F</sub>	I <sub>F</sub> = 30A, V <sub>GE</sub> = 0	_		2.0	V
Reverse Recovery Time		t <sub>rr</sub>	I <sub>F</sub> = 30A, V <sub>GE</sub> = 0 di / dt = -100A / µs	- (	54/	0.2	μs
Thermal Resistance (IGBT) R <sub>th (j-c)</sub>		R <sub>th (j-c)</sub>		-((	))~	1.67	°C / W
Thermal Resistance (Diode)		R <sub>th (j-c)</sub>		(7	4	2.27	°C / W







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